



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Serial No. .... 10/611,602  
Filing Date ..... June 30, 2003  
Inventor..... Michael J. Hermes  
Assignee..... Micron Technology, Inc.  
Group Art Unit..... 2812  
Examiner ..... Pompey, Ron Everett  
Attorney's Docket No. ..... MI22-2341  
Customer No. ..... 021567  
Title: Semiconductor Processing Methods of Forming Dynamic Random Access Memory (DRAM) Circuitry

**RESPONSE TO OCTOBER 18, 2005 FINAL OFFICE ACTION**  
**TO ACCOMPANY RCE FILING**

To: Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

From: Jennifer J. Taylor, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)  
Wells St. John P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3828

**AMENDMENTS**

**Introductory Comments**

In reply to the Final Office Action of October 18, 2005, applicant amends and remarks as follows.